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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Anthony J. Stratakos

Art Unit: 2838

Serial No.: 08/991,394

Examiner: Berhane, A.

Filed

: 12/16/97

Title : DISC

: DISCRETE-TIME SAMPLING OF DATA FOR USE IN SWITCHING

REGULATORS

Assistant Commissioner for Patents

Washington, DC 20231

## **RESPONSE**

In response to the Examiner's action mailed January 22% 1999, please amend the application, as follows:

## In the claims:

1. (Amended) A voltage regulator having an input terminal to be coupled to an input voltage source and an output terminal to be coupled to a load, comprising:

a power switch to alternately couple and decouple the input terminal to the output terminal; [with a variable duty cycle;]

a filter disposed between the input terminal and the output terminal to provide a substantially DC voltage at the output terminal;

a sampling circuit to make <u>and capture a measurement[s]</u> of an electrical characteristic of the voltage regulator at  $\underline{a}$  discrete moment[s] of time; and

a feedback circuit coupled to the sampling circuit and the

Date of Deposit 4/31 [49]
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Maried C/

Matilde C. Sanchez

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